

# Abstract of the Disclosure

A data conversion/output apparatus includes a large number of sensors, voltage-time conversion circuits, and sensed data generation circuits. The voltage-time conversion circuits are arranged adjacent to the respective sensors, and change output levels upon the lapse of times corresponding to output voltage values from the sensors after a conversion operation start point in order to convert the voltage outputs of the sensors into times. The sensed data generation circuits output, as digital data, lapse times until the output levels of the voltage-time conversion circuits change after a substantial conversion start point.

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